



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Om P. Agrawal; Hans W. Klein
 Assignee: Lattice Semiconductor Corporation
 Title: CLOCK GENERATOR
 Serial No.: 10/629,223 Filing Date: July 29, 2003
 Examiner: Cox, Cassandra F. Group Art Unit: 2816
 Docket No.: M-15169 US Confirmation No. 5335

Irvine, California
 January 23, 2006

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1. Part B – Fee Transmittal (1 page)
2. Fee Address “Indication Form” (1 page)
3. Change of Correspondence Address (1 page)

Dated: January 23, 2006

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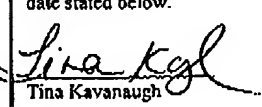
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Re:	Applicant(s):	Om P. Agrawal; Hans W. Klein
	Title:	Clock Generator
	Serial No.:	10/629,223
	Examiner:	Cox, Cassandra F.
	Docket No.:	M-15169 US
		Filed: 7/29/2003
		Group Art Unit: 2816
		Confirmation No. 5335


Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

1. Facsimile Cover Sheet (1 page)
2. Part B – Fee Transmittal (1 page)
3. Fee Address "Indication Form" (1 page)
4. Change of Correspondence Address (1 page)

Certificate of Mailing	
I hereby certify that this correspondence is being facsimile transmitted to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, facsimile no. 571-273-2885 on the date stated below.	
 Tina Kavanaugh	January 23, 2006

Respectfully submitted,



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